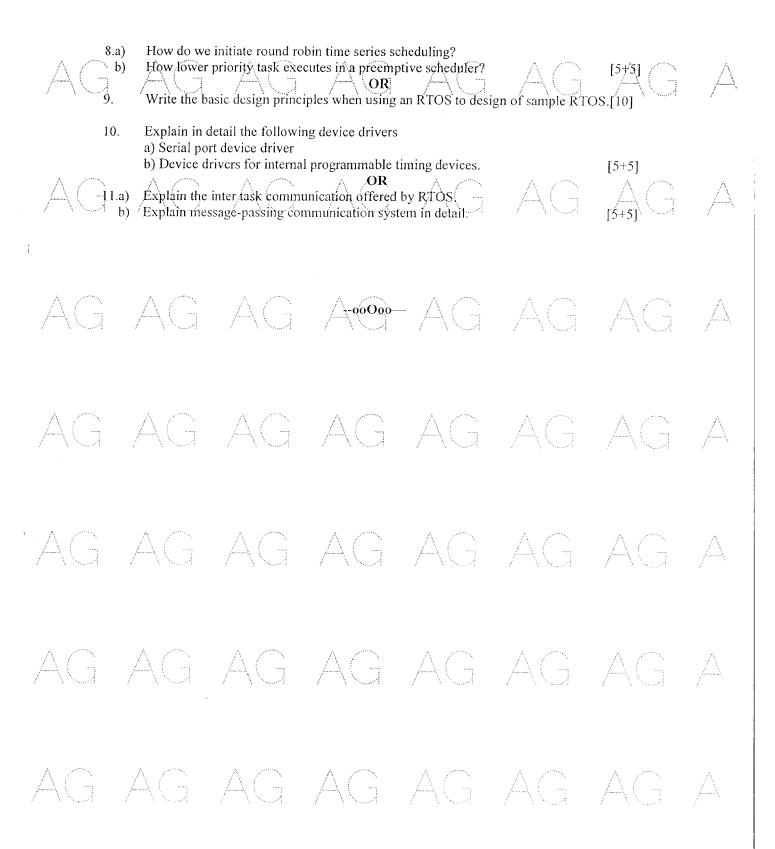
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	JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYD	ERABAD	
	B. Tech IV Year I Semester Examinations, November/December A EMBEDDED SYSTEM DESIGN A	- 2017	Λ
	(Common to ECE, ETM)		$-\Delta$
~ •	ime: 3 Hours ote: This question paper contains two parts A and B.	. Marks: 75	<i>'</i>
	Part A is compulsory which carries 25 marks. Answer all questions in I	Part A. Part B	
	consists of 5 Units. Answer any one full question from each unit. It carries 10 marks and may have a, b, c as sub questions.	Each question	
			Α
AG	AG AG PARTA AG A	(25 Marks)	A
1.a	List the characteristics of an embedded system. What is the difference between a system and an embedded system?	[2]	
c		[3] [2]	
d e		[3]	
	Explain the role of reset circuit in an embedded system. What is the difference between real time clock and watchdog time.	$ \begin{array}{ccc} & \begin{bmatrix} 2 \\ 3 \end{bmatrix} & \\ \end{array} $	
	g) /When do you use cooperative scheduling? / \\	$ \begin{array}{ccc} & [3] \\ & [2] \end{array} $	Journell
hj i)		[3] [2]	
j)	What is meant by concurrency of task execution in real time system?	[3]	
	DADE D		
AG	AGAGAGAGAGAG	(50 Marks)	A
2.a) b)	What are the components of Embedded System Hardware?	[5+5]	
3.	OR Discuss the purpose of embedded systems. List the design metrics used to	compare them	
A 2005	A CO	[10]	
$A(\frac{1}{4})$	With a neat diagram, explain the architecture of a general purpose processor		
and the second of the second o	OR	er de se deserve	
5.a) b)	5 1 1 1 1 mm an man speci	fic processors. in. [5+5]	
6.a)	What are the design criteria of external brown-out protection circuit.		
(How to design and implement firmware for embedded systems?	[5+\$]	\wedge
7.	How to design and implement firmware for embedded systems? OR Explain with one example, how to change the bus frequency of the processor		/\
		را∨ا	
	AG AG AG AG AC	<u> </u>	\wedge
z NSA		4 /7\\.	/

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b)

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[5+5]

[10]

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech IV Year I Semester Examinations, November/December - 2017 EMBEDDED SYSTEM DESIGN

(Common to ECE, ETM)

Time: 3 Hours Max. Marks: 75 **Note:** This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions. **PART-A (25 Marks)** 1.a) List the characteristics of an embedded system. [2] What is the difference between a system and an embedded system? b) [3] What is actuator? [2] c) d) What are the considerations for processor selection? [3] Explain the role of reset circuit in an embedded system. e) [2] What is the difference between real time clock and watchdog time. f) [3] When do you use cooperative scheduling? g) [2] What is the function of timer in RTOS? h) [3] What is Remote Procedure Call and explain its working? i) [2] What is meant by concurrency of task execution in real time system? j) [3] **PART-B (50 Marks)** 2.a) Explain the major application areas of embedded systems. b) What are the components of Embedded System Hardware? [5+5]OR 3. Discuss the purpose of embedded systems. List the design metrics used to compare them. [10] 4. With a neat diagram, explain the architecture of a general purpose processor. [10] Write the difference between general purpose processors and domain specific processors. 5.a) b) Discuss the aspects of memory allocation and mapping in embedded domain. [5+5]6.a) What are the design criteria of external brown-out protection circuit.

How to design and implement firmware for embedded systems?

Explain with one example, how to change the bus frequency of the processor.

8.a)	How do we initiate round robin time series scheduling?	
b)	How lower priority task executes in a preemptive scheduler?	[5+5]
	OR	
9.	Write the basic design principles when using an RTOS to design of sample RTOS	.[10]
10.	Explain in detail the following device drivers	
	a) Serial port device driver	
	b) Device drivers for internal programmable timing devices.	[5+5]
	OR	
11.a)	Explain the inter task communication offered by RTOS.	
b)	Explain message-passing communication system in detail.	[5+5]
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JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech IV Year I Semester Examinations, March - 2017

EMBEDDED SYSTEMS DESIGN

(Electronics and Communication Engineering)

Time: 3 Hours Max. Marks: 75

Note: This question paper contains two parts A and B.

firmware development languages.

Part A is compulsory which carries 25 marks. Answer all questions in Part A. Part B consists of 5 Units. Answer any one full question from each unit. Each question carries 10 marks and may have a, b, c as sub questions.

Part- A (25 Marks)

1.a)	Define Embedded System.	[2]
b)	List out the differences between an embedded system and a general purpose co	
		[3]
c)	Explain the concept of Memory Shadowing.	[2]
d)	Write a short note on COTS.	[3]
e)	What is the use of reset circuit in an embedded system?	[2]
f)	Briefly explain Brown-out protection circuit.	[3]
g)	What is the use of RTOS in Embedded System Design?	[2]
h)	Discuss briefly about Task Scheduling.	[3]
i)	What are the considerations to choose an RTOS?	[2]
j)	Discuss the issues in Task Synchronization briefly.	[3]
	Part-B (50 Marks)	
2.	Explain in detail the classification of embedded system. OR	[10]
3.a)	Describe the characteristics of an embedded system in detail.	
b)	Explain the quality attribute portability and reliability in embedded system	n design
	context.	[5+5]
4.a)	What are the different types of memories used in embedded system design? Exp with examples.	lain each
b)	Explain the role of sensors in embedded system design.	[5+5]
	OR	
5.	Explain the different communication interfaces with respect to embedded syste	ems. [10]
6.a) b)	Describe the purpose of a Real Time Clock in an embedded system, explain in Explain the function of Watchdog timer in an embedded system.	detail. [5+5]
7	OR What is the need of an embedded firmware? Briefly explain the	ambaddad
7.	what is the need of an embedded fifthware? Briefly explain the	embedaea

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8. What is a process? With a neat representation explain the process states and state transition. [10]

9. Explain the different thread binding models for user and kernel level threads. [10]

10.a) Explain message passing technique for inter process communication in detail.

b) Explain the concept of Shared memory in task communication. [5+5]

OR

11. What is a device driver? Explain the role of device driver in an embedded OS. [10]

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Code No: 117CZ

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD B. Tech IV Year I Semester Examinations, November/December - 2016 EMBEDDED SYSTEM DESIGN

	(Electronics and Communication Engineering)	
Time:	3 Hours Max. Ma	rks: 75
Note:	This question paper contains two parts A and B. Part A is compulsory which carries 25 marks. Answer all questions in Part B consists of 5 Units. Answer any one full question from each unit question carries 10 marks and may have a, b, c as sub questions.	
	PART- A	
	(25	Marks)
1.a)	Define "Time-to-market".	[2]
b)	What is the quality attribute "Portability" in the embedded system design conte	xt. [3]
c)		[2]
d)	•	[3]
e)		[2]
f)	•	[3]
g)		[2]
h)		[3]
i)	` '	[2]
j)		[3]
	PART-B	
	(50	Marks)
2.	Define an embedded system? Explain the characteristics of Embedded Systems. OR	[10]
3.	Explain the various purposes of embedded systems in detail with illustrative exam	ples. [10]
4.a)	Explain the different factors that needs to be considered in the selection of men embedded systems.	nory for
b)	Explain the difference between I ² C and SPI communication interface. OR	[5+5]
5.	Explain the different communication buses used in automotive application.	[10]

6. Explain the different sections of a memory segment allocated to an application by the memory manager. [10]

OR

- 7. Explain the difference between 'pointer to constant data' and 'constant pointer to data' in Embedded C programming. Explain the syntax for declaring both. [10]
- 8.a) Explain starvation in the process scheduling context. Explain how starvation can be effectively tackled.
 - b) What is the difference between a General Purpose kernal and Real-Time kernel? Give an example for both. [5+5]

OR

- 9. Explain the different multitasking models in the operating system context. [10]
- 10. Explain in detail, the different task communication synchronization issues encountered in Inter Process communication. [10]

OR

11. Explain the architecture of device driver, with neat sketch and give the applications of device drivers. [10]

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